

Fig. 1

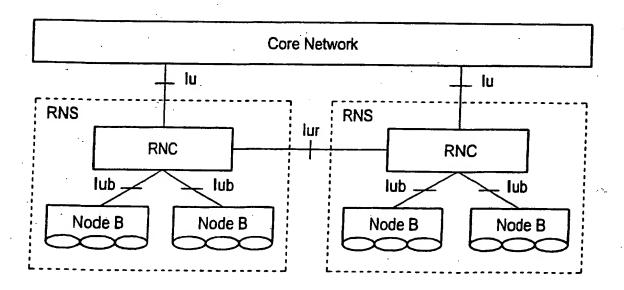


Fig. 2

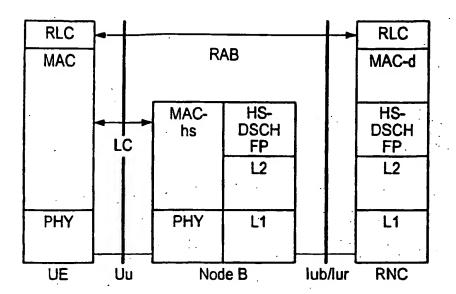


Fig. 3

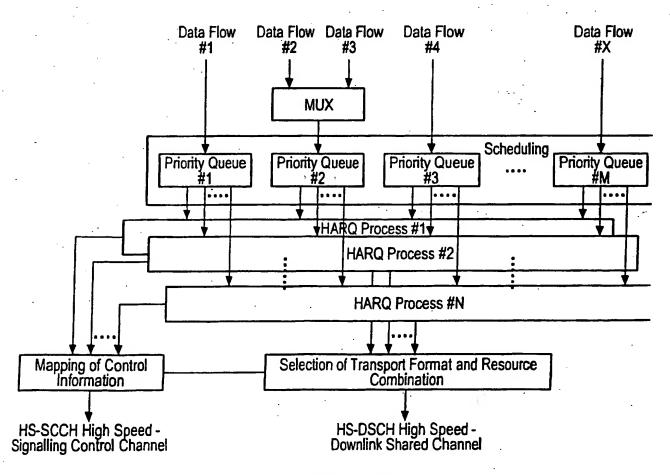
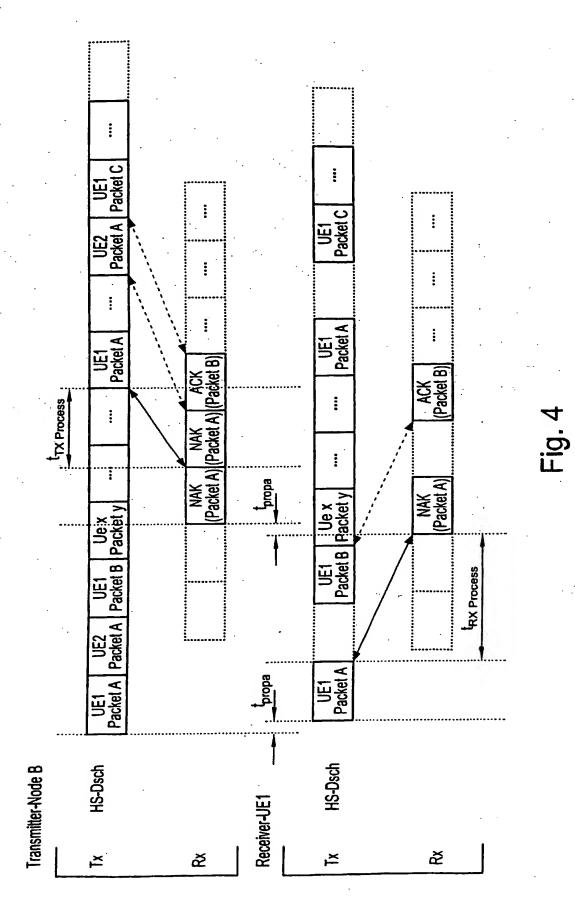


Fig. 5



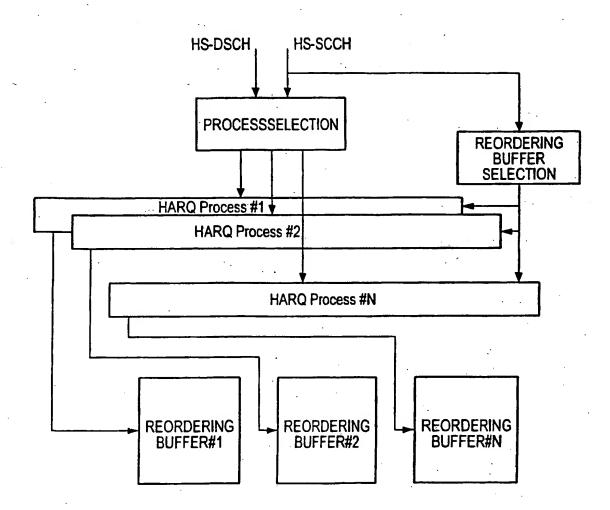


Fig. 6

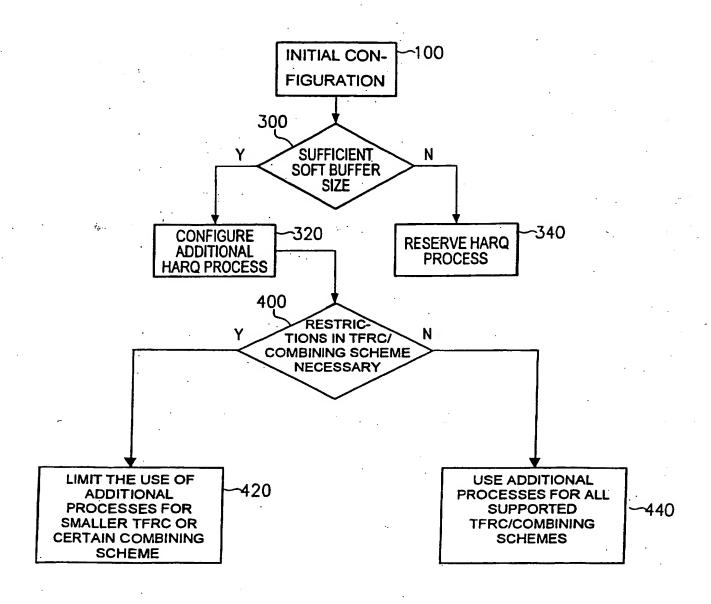


Fig. 7

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E/I Memory partitioning	E/I	Reordering Buffer Configuration
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Fig. 8

1	2	3	4	5	free	case a
1		2	3	4	5	case b
1	2	3	4	5	Al A2	case c

Fig. 9